Form PTO-1449

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

ATTY. DOCKET NO. 32809

SERIAL NO. 09/615,938

INFORMATION DISCLOSURE CITATION BY APPLICANT (USE SEVERAL SHEETS IF NECESSARY)

APPLICANT:

Kenji Shimazaki et al.

Page 1 of 1					FILING DATE: July 13, 2000			GROUP ART UNIT: 2857	
U.S. PATENT DOCUMENTS									
Examiner Initial		Document No.	Date	Na	ime	Class	Subclass		Filing Date
	Α								
	В					[RECEI		VED
	С						ΔΙ	IG 1 9	2004
	D		•			OF	EIC	FOFF	ETITIONS
j 	E					V			
	F						<u> </u>		
	G								
	Н								
	I								
	J						_		
	K						L		
	L								
FOREIGN PATENT DOCUMENTS									
		Document No.	Date	Cou	intry	Class	Subclass		Translation
	М						_		
	N								
	P								
	Q								
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)									
M (B	R	Synopsys: Prime Power, Full-Chip Dynamic Power Analysis for Multimillion-Gate Designs, 2004.							
	S								
	T						Ţ		
Examiner: Many Cathurinu Baraga Date Considered 2/23/05									

*Examiner:

Initial if reference considered, regardless of whether citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.